



EXPEDITED PROCEDURE - EXAMINING GROUP 2815

S/N 09/346,283

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Michael R. Flannery et al.	Examiner:	Jose R. Diaz
Serial No.:	09/346,283	Group Art Unit:	2815
Filed:	July 1, 1999	Docket:	450.202US1
Title:	INTEGRATED CIRCUIT WITH UNIFIED INPUT DEVICE, MICROPROCESSOR AND DISPLAY SYSTEMS		

AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

Box AF
Commissioner for Patents
Washington, D.C. 20231

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In response to the final Office Action mailed January 15, 2002, please amend the application as follows:

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect the amendment of previously pending claims 1 and 4-6. The specific amendments to individual claims are detailed in the following marked up set of claims.

1. (Twice Amended) An integrated circuit with a micromechanical element comprising a semiconductor support substrate supporting a sensor element, a logic circuit and a semiconductor visual display element, the sensor element electrically connected to the logic circuit, and the logic circuit being electrically connected to the semiconductor visual display element[, said sensor element for providing an input to said logic circuit in response to a mechanical actuation of said sensor element].
4. (Twice Amended) The integrated circuit of claim 1 wherein said visual display element comprises an array of semiconductor pixels having [diameter] pitch dimensions of less than 20 micrometers.